

Notice of Allowability	Application No.	Applicant(s)	
	09/918,792	CASTILLEJO ET AL.	
	Examiner Benny Q. Tieu	Art Unit 2642	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. This communication is responsive to the response to Election/ Restriction filed April 12, 2005.
2. The allowed claim(s) is/are 1-9.
3. The drawings filed on 30 July 2001 are accepted by the Examiner.
4. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All b) Some* c) None of the:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
6. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) hereto or 2) to Paper No./Mail Date _____.
 - (b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. Notice of References Cited (PTO-892)
2. Notice of Draftperson's Patent Drawing Review (PTO-948)
3. Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date 3/19/02
4. Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. Notice of Informal Patent Application (PTO-152)
6. Interview Summary (PTO-413),
Paper No./Mail Date _____.
7. Examiner's Amendment/Comment
8. Examiner's Statement of Reasons for Allowance
9. Other _____.

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

2. The application has been amended as follows:

Claims 10-14 have been canceled.

(End of Amendment)

Election/Restrictions

3. Applicant's election without traverse of invention I in the reply filed on April 12, 2005 is acknowledged.
4. This application is in condition for allowance except for the presence of claims 10-14 to an invention non-elected without traverse. Accordingly, claims 10-14 have been cancelled.

Reasons For Allowance

5. The following is an examiner's statement of reasons for allowance: Uemura et al. (U.S. Patent No. 5,874,883) teach a planar-type inductor that can restrain the maximum operable frequency from lowering even if the inductance part of the inductor is miniaturized. This inductor includes an insulating substructure and a patterned conductor layer having a predesigned geometric shape and formed on a chief surface of the substructure. The patterned conductor layer contains an inductance part for providing a wanted inductance value and a first lead part and a second lead part for electrically connecting the inductance part to an external circuit located outside the inductor. The inductance part has a first region and a second region located adjacent to each other at an interval. The inductor further includes a recess formed in the substructure between the first region and the second region of the inductance part. The recess extends along the first region and the second region. A parasitic capacitance between the first region and the second region is reduced due to the existence of the recess. Another patterned conductor layer extending along the pattern of the inductance part of the may be formed, instead of the recess, which reduces the parasitic capacitance due to the electrostatic shielding

phenomenon (column 3, line 15 to column 4, line 23). However, prior art of record fails to teach, or renders obvious, alone or in combination, an integrated circuit including an inductance structure on a semiconductor substrate comprises a first inductance and second inductance formed by a conductive track having first, second ends and intermediate point that connect to first, second, and third terminals and means for setting the third terminal to high impedance when the first inductance is used as directly claimed in independent claim 1.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Benny Q. Tieu whose telephone number is 571-272-7490. The examiner can normally be reached on Monday-Friday: 6:30AM - 5:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Ahmad Matar can be reached on 571-272-7488. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Art Unit: 2642

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Benny Q. Tieu
Primary Examiner
Art Unit 2642